

[54] GRAPHIC DISPLAY UNIT

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[56] References Cited

U.S. PATENT DOCUMENTS

4,070,662 1/1978 Narveson 340/726
4,320,395 3/1982 Meissen et al. 340/726

4,412,294 10/1983 Watts et al. 340/726

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[57] ABSTRACT

A graphic display unit having a shifting circuit for shifting a picture image to a designated position on or off a display panel, the shifting circuit including a signal delaying circuit for delaying a divided clock signal obtained by dividing a main clock signal and for delaying a display timing signal in accordance with the designated amount of shift of the picture image, whereby, in response to the delayed divided clock signal and the delayed display timing signal, the data of the picture image is read from a graphic random access memory.

3 Claims, 13 Drawing Figures

